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1/10/05
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert Everest Johnson et al.
Serial No: 10/679,873
Filed: October 6, 2003
Confirmation No.: 3837
Art Unit: 2817
Title: ARCHITECTURE AND IMPLEMENTATION METHODS OF DIGITAL
PREDISTORTION CIRCUITRY
Atty Docket: ANCO-89US

Cincinnati, Ohio 45202

January 10, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. §§§ 1.56, 1.97 and 1.704, the references listed on the attached supplemental Form PTO-1449 are called to the attention of the United States Patent and Trademark Office in connection with the above-identified patent application. Copies of the cited references are attached hereto.

The Examiner is respectfully requested to review each reference cited and to note the review on the attached PTO-1449 form and to reach their own independent determination of its materiality, if any.

A copy of the Search Report in counterpart British Patent Application Serial No. GB 0416231.9 is enclosed. This Search Report was received in our office

December 10, 2004.

No fee is due for this submission in accordance with C.F.R. 37,
§§ 1.97(e)(2) and 1.704(d).

Certification under 37 C.F.R. § 1.97(e)(2)

Each item of information contained in the Supplemental Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.


Certification under 37 C.F.R. § 1.704(d)

The foreign patent office communication cited herein was not received by any individual designated in 1.56(c) more than thirty days prior to submission of this Supplemental Information Disclosure Statement.

Applicant knows of no fees due with this submission. However, should any fees be deemed required, the Commissioner is hereby authorized to charge any such fees to Deposit Account No. 23-3000.

Respectfully submitted,

WOOD, HERRON & EVANS, L.L.P.

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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
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STATEMENT BY APPLICANTAPPLICANT
Robert Everest Johnson et
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(37 CFR 1.98(b))

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.A						
	A.B						
	A.C						
	A.D						
	A.E						
	A.F						
	A.G						
	A.H						
	A.I						
	A.J						
	A.K						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION (YES/NO)
A.L	EP 1 162 732 A3	12.12.2001	EPO	H03F	1/32	
A.M	GB 2 400 509	10.13.2004	Great Britain	H03F	1/32	
A.N	WO 02/095932	11.28.2002	International	H03F	1/26	
A.O						
A.P						
A.Q						

OTHER DOCUMENTS (Including Author, Title, Date, Place of Publication)

A.R	US2003/0146787 A1, U.S. Patent Application to Hedberg et al. Filed February 6, 2002, POWER AMPLIFIER LINEARIZER THAT COMPENSATES FOR LONG-TIME-CONSTANT MEMORY EFFECTS AND METHOD THEREFOR
A.S	British Search Report, November 29, 2004
A.T	US2003/0058959 A1, U.S. Patent Application to Rafie et al. Filed September 25, 2001, COMBINED DIGITAL ADAPTIVE PRE-DISTORTER AND PRE-EQUALIZER SYSTEM FOR MODEMS IN LINK HOPPING RADIO NETWORKS
EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not in conformance. Draw line through citation only if not in conformance and not considered.
Include a copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)